





# Differential Testing of Pushdown Reachability with a Formally Verified Oracle

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**Abstract**—Pushdown automata are an essential model of recursive computation. In model checking and static analysis, numerous problems can be reduced to reachability questions about pushdown automata and several efficient libraries implement automata-theoretic algorithms for answering these questions. These libraries are often used as core components in other tools, and therefore it is instrumental that the used algorithms and their implementations are correct. We present a method that significantly increases the trust in the answers provided by the libraries for pushdown reachability by (i) formally verifying the correctness of the used algorithms using the Isabelle/HOL proof assistant, (ii) extracting executable programs from the formalization, (iii) implementing a framework for the differential testing of library implementations with the verified extracted algorithms as oracles, and (iv) automatically minimizing counter-examples from the differential testing based on the delta-debugging methodology. We instantiate our method to the concrete case of PDAAAL, a state-of-the-art library for pushdown reachability. Thereby, we discover and resolve several nontrivial errors in PDAAAL.

## I. INTRODUCTION

In 1964, Büchi [7] proved that the possibly infinite set of all reachable pushdown configurations (from a given initial configuration) can be effectively described by a regular language. In fact, even for a given regular set of pushdown configurations, its  $post^*$  and  $pre^*$  closures (representing all forward and backward reachable configurations from a given set of configurations) are also regular. Büchi’s *automata-theoretic approach* gave rise to a rich theory of pushdown reachability with numerous algorithms and applications to, e.g., interprocedural control-flow analysis of recursive programs [9], [11], model checking [4], [13], [45], [46], communication network analysis [10], [21], [22] and others. A number of tools have been developed to support the theory, including Moped [45], [46], WALi [25], and PDAAAL [23] with applications ranging from the static analysis of Java [46] and C/C++ code [26], [43] to the analysis of MPLS communication protocols [22].

Even though the automata-theoretic approach for pushdown reachability is based on relatively simple saturation procedures, the proofs of correctness are nontrivial and the implementation of the algorithms in the different tools often includes numerous performance optimizations as well as additional improvements to the theory itself [23]. To be able to rely on

the output of model checking tools and other applications of pushdown reachability, it is important that the theory is not only sound but also correctly implemented. A positive reachability answer is typically accompanied by a finite evidence (trace) that can function as an efficiently checkable certificate. A negative answer is, on the other hand, much harder to check, and designing a finite evidence for non-reachability is difficult, primarily because the number of reachable pushdown configurations can be infinite. One approach is to establish an invariant that (i) includes the initial configuration(s) of the system, (ii) is maintained by the transition relation and (iii) has an empty intersection with the set of undesirable configurations. Such approaches have been studied [16], [17] but are usually incomplete and require another complex tool (that can be error-prone, too) to verify such invariants.

We instead use a proof assistant, Isabelle/HOL [37] (§II), to formally verify the correctness of the pushdown reachability algorithms  $post^*$  (forward search),  $pre^*$  (backward search), and  $dual^*$  (bi-directional search) (§III) that lie at the heart of the automata-theoretic analysis of pushdown systems [4], [23], [44]. From the formalization of  $pre^*$ , we extract an executable program with strong correctness guarantees (§IV). For a given input, the extracted program’s output can be compared with the output of other, unverified but optimized tools solving the same problem (§V). This approach is known as differential testing [14], [18], [34] with a twist that the testing oracle has been formally verified and thus is extremely trustworthy. When testing reveals a disagreement between a verified and an unverified algorithm, we know who is to blame. To help localize errors in unverified algorithms, we minimize the tests causing disagreement using the delta-debugging technique [51]. Our main contributions are as follows.

- The formalization of  $post^*$ ,  $pre^*$  and  $dual^*$  algorithms in Isabelle/HOL and verification of their correctness based on the proofs provided by Schwoon [44] for  $post^*$  and  $pre^*$ , and following Jensen et al. [23] for  $dual^*$ .
- The refinement to and the extraction of an executable program of the formalized  $pre^*$  algorithm that serves as the verified oracle for differential testing.
- The automatic minimization of the input automata in cases where an unverified tool disagrees with the oracle.

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- The application of our method to a modern state-of-the-art library for pushdown reachability, PDAAAL [23], and the identification, localization (using the minimized counter-examples), and correction of three, previously unknown, implementation errors (§VI). The corrected implementation passes all differential tests successfully.

Our Isabelle formalization as well as the case study are publicly available [40].

a) **Related work.** Differential testing with a verified oracle has been used in the context of runtime verification and automatic theorem proving. The runtime monitor VeriMon [2], [41] served as the verified oracle used to detect errors in unverified monitors. Compared to our approach, VeriMon’s differential testing case study is from a different application domain, does not include exhaustive test generation for small input sizes (which is difficult in runtime monitoring) and does not minimize the tests automatically. To assess its performance but also to evaluate the benchmark’s correctness, the verified first-order prover RPx [39] was evaluated on a standard benchmark for first-order logic problems. RPx’s answers have in all cases coincided with the expected ones recorded in the benchmark.

The verified C compiler CompCert [32] and several verified distributed systems [20], [33], [48] have been themselves put onto the testbed [15], [50]. A few errors in these tools’ unverified parts or in scenarios violating the verification assumptions were found, but none in the verified components themselves.

Many works extract efficient executable code from formalizations, but do not use it as an oracle in testing. Examples include verified model checkers for LTL [12] and timed automata [49] and verified algorithms for finite automata [3], [6], [24], [31] and context-free grammars [35], [38].

The only formalization of pushdown automata we are aware of is part of Lammich et al.’s work on dynamic pushdown networks (DPN) [30]. Lammich describes the Isabelle formalization of an executable  $pre^*$  algorithm for DPNs stemming from this work in an unpublished technical report [29]. DPNs generalize pushdown automata, but their  $post^*$  is not regular [5] and so we cannot extend this work for our purposes. Moreover, Lammich’s formalization does not support  $\varepsilon$ -transitions in the underlying automata, an essential component needed for our formalization of  $post^*$  and  $dual^*$ .

b) **Background definitions.** Let  $P$  be a finite set of control locations and  $\Gamma$  a finite stack alphabet. A *pushdown system* (PDS) is a tuple  $(P, \Gamma, \Delta)$ , where  $\Delta \subseteq (P \times \Gamma) \times (P \times \Gamma^*)$  is a finite set of *rules*, written  $(p, \gamma) \hookrightarrow (q, w)$  whenever  $((p, \gamma), (q, w)) \in \Delta$ . Without loss of generality, we assume  $|w| \leq 2$ , so that  $w = \varepsilon$  represents a *pop* operation that removes the topmost stack symbol,  $|w| = 1$  is a *swap* that replaces the topmost symbol with another one, and  $|w| = 2$  is a *push* that incorporates a *swap* followed by adding a new symbol on top.

A *configuration* of a pushdown system is a pair  $(p, w)$  of the current control location  $p \in P$  and the current stack content  $w \in \Gamma^*$  where we assume that the top of the stack is on the left. The set of all configurations is denoted by  $\mathcal{C}$ . A PDS can take a *computation step*  $(p, \gamma w') \Rightarrow (q, ww')$  between configurations whenever  $(p, \gamma) \hookrightarrow (q, w)$  and  $w' \in \Gamma^*$ . For a given  $C \subseteq \mathcal{C}$ ,

we define  $post^*(C) = \{c' \in \mathcal{C} \mid c \Rightarrow^* c' \text{ for some } c \in C\}$  and  $pre^*(C) = \{c \in \mathcal{C} \mid c \Rightarrow^* c' \text{ for some } c' \in C\}$ .

The *reachability problem* for PDSs is to decide whether  $c \Rightarrow^* c'$  for configurations  $c$  and  $c'$ , and it is equivalent to asking whether  $c' \in post^*({c})$  or equivalently whether  $c \in pre^*({c'})$ . Büchi [7] showed that for any regular set  $C \subseteq \mathcal{C}$ , the sets  $post^*(C)$  and  $pre^*(C)$  are also regular.

To represent regular sets of pushdown configurations, we use *P-automata* [44], which are nondeterministic finite automata with multiple initial states for each of the control locations from the set  $P$ . Formally, let  $N$  be a finite set of noninitial states and  $F \subseteq P \cup N$  a finite set of final states. A *P-automaton* is a tuple  $\mathcal{A} = (P \cup N, \rightarrow, P, F)$  with the transition relation  $\rightarrow \subseteq (P \cup N) \times \Gamma \times (P \cup N)$  so that  $P \cup N$  is the set of its states and the pushdown alphabet  $\Gamma$  is the input alphabet of the automaton. The language  $L(\mathcal{A})$  of *P-automaton*  $\mathcal{A}$  contains the pushdown configurations accepted by  $\mathcal{A}$ : a configuration  $(p, w) \in P \times \Gamma^*$  is accepted if and only if there is a path from  $p$  to  $q$  for some  $q \in F$  in the *P-automaton* (defined via the transition relation  $\rightarrow$ ) labelled with  $w$ . The *reachability problem for P-automata* is as follows: given a PDS  $(P, \Gamma, \Delta)$  and *P-automata*  $\mathcal{A}_1$  and  $\mathcal{A}_2$ , does there exist  $c \in L(\mathcal{A}_1)$  and  $c' \in L(\mathcal{A}_2)$  such that  $c \Rightarrow^* c'$  using the rules  $\Delta$ ?

## II. ISABELLE/HOL

Isabelle/HOL [37] is a proof assistant based on classical higher-order logic (HOL), a simply typed lambda calculus with Hilbert choice, axiom of infinity, and rank-1 polymorphism. We present our formalization using HOL’s syntax, which mixes functional programming and mathematical notation.

Types are built from type variables  $'a, 'b, \dots$  and type constructors like pairs  $\_ \times \_$  and functions  $\_ \Rightarrow \_$  (both written infix) and sets  $\_ \text{ set}$  (written postfix). Type constructors can also be nullary, e.g., the Boolean type *bool*. Type variables can be restricted by type classes:  $'a :: \text{finite}$  is a type variable  $'a$  that can only be instantiated with finite types (i.e., types with finitely many inhabitants). New type constructors are introduced as abbreviations for complex type expressions and as inductive datatypes using commands **type synonym** and **datatype** respectively, e.g., the types of transitions **type synonym**  $(\text{'state}, \text{'label}) \text{ transition} = \text{'state} \times \text{'label} \times \text{'state}$  and finite lists **datatype**  $\text{'a list} = [] \mid \text{'a} \# (\text{'a list})$ .

Terms are built from variables  $x, y, \dots$ , constants  $c, d, \dots$ , lambda abstractions  $\lambda x. t$  and applications written as juxtaposition  $f x$ . Isabelle includes many constants and syntax for them, e.g., infix operators  $\wedge, \vee, \longrightarrow, \longleftarrow, \in, \cup, \cap$ , unbounded and bounded quantifiers  $\exists x. P x$  and  $\forall y \in A. Q y$ , and set comprehensions  $\{x. P x\}$ . Non-recursive functions are defined and given readable syntax using the **definition** command:

**definition** image (infix  $'$ ) **where**

$$f \text{ ' } A = \{y. \exists x \in A. y = f x\}$$

Type annotations like  $\text{image} :: (\text{'a} \Rightarrow \text{'b}) \Rightarrow \text{'a set} \Rightarrow \text{'b set}$  can be omitted as they are inferred. Recursive definitions are supported using the **fun** command:

**fun** append (infix  $@$ ) **where**

$$[] @ ys = ys \mid (x \# xs) @ ys = x \# (xs @ ys)$$

```

locale LTS = fixes trans_rel :: ('state, 'label) transition set
begin
  definition step_relp (infix  $\Rightarrow$ ) where
     $c \Rightarrow c' \iff (\exists l. (c, l, c') \in \text{trans\_rel})$ 
  definition step_starp (infix  $\Rightarrow^*$ ) where
     $c \Rightarrow^* c' \iff \text{step\_relp}^{**} c c'$ 
  definition pre_star  $C = \{c'. \exists c \in C. c' \Rightarrow^* c\}$ 
  definition post_star  $C = \{c'. \exists c \in C. c \Rightarrow^* c'\}$ 
  definition srcs =  $\{p. \nexists q \gamma. (q, \gamma, p) \in \text{trans\_rel}\}$ 
  definition sinks =  $\{p. \nexists q \gamma. (p, \gamma, q) \in \text{trans\_rel}\}$ 
  inductive_set trans_star where
     $(p, [], p) \in \text{trans\_star}$ 
    |  $(p, \gamma, q') \in \text{trans\_rel} \longrightarrow (q', w, q) \in \text{trans\_star} \longrightarrow$ 
     $(p, \gamma \# w, q) \in \text{trans\_star}$ 
end

```

Fig. 1: The locale for labeled transition systems

Internally, **fun** performs an automatic termination proof. More complex recursion schemes may require a manual proof.

Another way to define a function is as Prolog-style monotone rules. The **inductive** command allows such definitions as least fixed points. Take, e.g., the reflexive transitive closure:

```

inductive rtranclp (_**) where
   $R^{**} x x \mid R x y \longrightarrow R^{**} y z \longrightarrow R^{**} x z$ 

```

Theorems and lemmas are terms of type *bool* that have been proved to be equivalent to *True*. All proofs pass through Isabelle’s kernel, which relies only on a few well-understood reasoning rules such as modus ponens. We refer to a textbook [36] for a practical introduction to proving in Isabelle.

Structures and assumptions common to many theorems can be organized via locales [1]—Isabelle’s module mechanism for fixing parameters and stating and assuming their properties. In the context of a locale, the parameters are available as constants and the assumptions as facts. Locales can be interpreted, which involves instantiating the parameters and proving the assumptions. As the result, one obtains the (instantiated) theorems proved in the context of the locale.

Consider our locale for labeled transition systems (LTSs) in Fig. 1. It fixes the parameter *trans\_rel*, and its context consists of the definitions between the **begin** and **end** keywords. All definitions should be self-explanatory except perhaps *trans\_star*: the set of triples  $(p, w, q)$  for which the LTS can move from  $p$  to  $q$  by consuming word  $w$ . This relation is defined inductively, first for the empty sequence and then extending it by one more symbol—here we use in conjunction two assumptions on the symbol  $\gamma$  and sequence  $w$ . (Following an Isabelle convention, we formalize it equivalently as two implications.) In the formalization, the locale has more definitions than shown here and a number of lemmas. Outside LTS’s context, we can access its definitions, e.g., *pre\_star* is available under the name *LTS.pre\_star* and can be applied to any transition relation  $A$  and a set of states  $C$  as follows: *LTS.pre\_star A C*.

```

datatype 'label op = pop | swap 'label | push 'label 'label
type_synonym ('ctr_loc, 'label) rule =
  ('ctr_loc  $\times$  'label)  $\times$  ('ctr_loc  $\times$  'label op)
type_synonym ('ctr_loc, 'label) conf = 'ctr_loc  $\times$  'label list
locale PDS = fixes  $\Delta :: ('ctr\_loc, 'label:: \text{finite}) \text{ rule set}$ 
begin
  fun lbl where
     $\text{lbl pop} = [] \mid \text{lbl (swap } \gamma) = [\gamma] \mid \text{lbl (push } \gamma \gamma') = [\gamma, \gamma']$ 
  definition is_rule (infix  $\hookrightarrow$ ) where
     $(p, \gamma) \hookrightarrow (p', w) \iff ((p, \gamma), (p', w)) \in \Delta$ 
  inductive_set step where
     $(p, \gamma) \hookrightarrow (p', w) \longrightarrow$ 
     $((p, \gamma \# w'), (), (p', \text{lbl } w @ w')) \in \text{step}$ 
  interpretation LTS step .
end

```

**end**

```

datatype ('ctr_loc, 'noninit) state =
  Init 'ctr_loc | Noninit 'noninit
locale PDS_with_finals = PDS  $\Delta$ 
  for  $\Delta :: ('ctr\_loc :: \text{enum}, 'label :: \text{finite}) \text{ rule set} +$ 
  fixes F_inits :: 'ctr_loc set and F_noninits :: 'noninit set
begin
  definition finals = Init ‘ F_inits  $\cup$  Noninit ‘ F_noninits
  definition inits =  $\{q. \exists p. q = \text{Init } p\}$ 
  definition accepts  $A (p, w) =$ 
     $(\exists q \in \text{finals}. (\text{Init } p, w, q) \in \text{LTS.trans\_star } A)$ 
  definition lang  $A = \{c. \text{accepts } A c\}$ 
end

```

Fig. 2: The types and locales for pushdown systems

### III. PUSHDOWN REACHABILITY

We formalize pushdown systems (PDSs) and saturation algorithms for calculating *pre\** and *post\** following Schwoon [44] and *dual\** following Jensen et al. [23].

Fig. 2 shows our modeling of PDSs. We use type variables to represent control locations (*'ctr\_loc*) and stack labels (*'label*). We introduce types for operations (*'label op*), rules (*('ctr\_loc, 'label) rule*) and configurations (*('ctr\_loc, 'label) conf*). A PDS is given by the locale *PDS*, which fixes a set of rules  $\Delta$ . Each PDS gives rise to an unlabeled transition relation, which we model by an LTS step with label  $()$ —the only element of type *unit*. The definition is a non-recursive **inductive** definition. We use the **interpretation** command to interpret LTS with *step*. This means that *pre\_star* refers to *LTS.pre\_star* step in *PDS*. Likewise, *trans\_star* refers to *LTS.trans\_star* step and similarly for other LTS definitions. The type *('ctr\_loc, 'noninit) state* represents *P*-automata states, where *'noninit* is the type variable for noninitial states. The locale *PDS\_with\_finals* extends *PDS* with a set of final initial states *F\_inits* and final noninitial states *F\_noninits*. For the rest of this section, we work within the *PDS\_with\_finals* locale. In this locale, a *P*-automaton is a set of transitions.

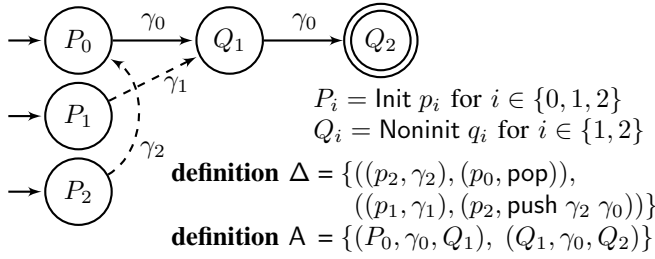


Fig. 3: Adding two transitions (dashed arrows) to a  $P$ -automaton. Initially (solid arrows) the  $P$ -automata encodes only configuration  $(p_0, [\gamma_0, \gamma_0])$ . After saturation, the configurations  $(p_1, [\gamma_1, \gamma_0])$  and  $(p_2, [\gamma_2, \gamma_0, \gamma_0])$  are also encoded.

### A. Nondeterministic $pre^*$ Saturation

Schwoon [44] presents the  $pre^*$  saturation which is a nondeterministic algorithm that given a  $P$ -automaton  $A$  returns a  $P$ -automaton whose language is  $pre\_star(\text{lang } A)$ . The algorithm proceeds by iteratively adding transitions to  $A$ . In each step, the algorithm nondeterministically chooses a transition to add that satisfies a number of criteria. The  $P$ -automaton is saturated when no more transitions can be added. We formalize a step of the algorithm by the relation:

**inductive pre\_star\_rule where**

$$\begin{aligned} &(\text{Init } p, \gamma, q) \notin A \longrightarrow (p, \gamma) \hookrightarrow (p', w) \longrightarrow \\ &(\text{Init } p', \text{lbl } w, q) \in \text{LTS.trans\_star } A \longrightarrow \\ &\text{pre\_star\_rule } A \ (A \cup \{(\text{Init } p, \gamma, q)\}) \end{aligned}$$

The  $pre\_star\_rule$  relation relates two  $P$ -automata if the latter can be obtained from the former via one step of the algorithm. The criteria of the algorithm are expressed as the premises of the implication shown in  $pre\_star\_rule$ 's definition. The last two premises are taken directly from Schwoon's definition of the algorithm and the first one ensures that the transition we add into the new  $P$ -automaton is a new one. A single  $P$ -automaton can be related to different  $P$ -automata via  $pre\_star\_rule$ , which captures nondeterministic choice.

Consider the PDS defined by  $\Delta$  in Fig. 3, and let the  $P$ -automaton  $A$  consist of the two solid transitions in the figure. Let  $A'$  be  $A \cup \{(P_2, \gamma_2, P_0)\}$ . Notice that  $(P_2, \gamma_2, P_0) \notin A$  and  $(p_2, \gamma_2) \hookrightarrow (p_0, \text{pop})$  and  $(P_0, \text{lbl } \text{pop}, P_0) \in \text{LTS.trans\_star } A$ . From  $pre\_star\_rule$ 's definition then follows that  $pre\_star\_rule \ A \ A'$ . Let  $A''$  be  $A' \cup \{(P_1, \gamma_1, Q_1)\}$ . From  $pre\_star\_rule$ 's definition it follows that  $pre\_star\_rule \ A' \ A''$ .

We formalize what it means for a  $P$ -automaton  $A$  to be saturated w.r.t a rule  $r$ , and for  $A'$  to be a saturation of  $A$ :

**definition saturated**  $r \ A = (\nexists A'. \ r \ A \ A')$

**definition saturation**  $r \ A \ A' = (r^{**} \ A \ A' \wedge \text{saturated } r \ A')$

In our example,  $A''$  is saturated and thus formally we have  $\text{saturated } pre\_star\_rule \ A''$  and  $\text{saturation } pre\_star\_rule \ A \ A''$ .

We next prove the  $pre^*$  saturation algorithm correct. Here, we focus on the proof's most interesting aspects, especially those where we had to deviate from Schwoon's pen-and-paper proof, and refer to our formalization for full details [40].

The correctness theorem states that if a transition system  $A'$  is a saturation of a transition system  $A$  then the language

of  $A'$  is indeed the  $pre^*$  closure of the language of  $A$ . Like Schwoon, we assume that the initial states are sources:

**theorem pre\_star\_rules\_correct:**

**assumes**  $\text{inits} \subseteq \text{LTS.srcs } A$

**and** saturation  $pre\_star\_rule \ A \ A'$

**shows**  $\text{lang } A' = pre\_star(\text{lang } A)$

Schwoon's Lemma 3.1 is used to prove the  $\supseteq$  direction of the theorem's conclusion. He proves it by considering an arbitrary predecessor configuration  $(p', w)$  of a configuration  $(p, v)$  in  $A$ 's language. The proof proceeds by induction on the number of  $\Rightarrow$  transitions from  $(p', w)$  to  $(p, v)$ . We do not keep track of this number, but we instead prove the lemma by induction on the transitive and reflexive closure of  $\Rightarrow$ . The formalization of the proof is written in Isabelle's structured proof language Isar (not shown) and follows Schwoon's arguments. Schwoon's Lemma 3.2 is used to prove the  $\subseteq$  direction of  $pre\_star\_rules\_correct$ 's conclusion. We showcase Lemma 3.2 in Schwoon's formulation, but adapted to our notation:

**Lemma 3.2** If saturation  $pre\_star\_rule \ A \ A'$  and

$(p, w, q) \in \text{LTS.trans\_star } A'$  then:

- (a)  $(p, w) \Rightarrow^* (p', w')$  for a configuration  $(p', w')$  such that  $(p', w', q) \in A$ ;
- (b) moreover, if  $q$  is an initial state, then  $w' = []$ .

In his proof, Schwoon claims to prove (a) by an induction and then that (b) will follow immediately from a simple argument. However, reading his proof we notice that he uses (b) in the proof of (a). We resolve this by noticing that we can strengthen (b) to hold for any stack  $w$  and not just the one  $w'$  claimed to exist in (a). Our formulation of (b) looks as follows:

**lemma word\_into\_init\_empty:**

**assumes**  $(p, w, \text{Init } q) \in \text{LTS.trans\_star } A$

**and**  $\text{inits} \subseteq \text{LTS.srcs } A$

**shows**  $w = [] \wedge p = \text{Init } q$

We prove (a) using the strengthened version of (b). Like Schwoon, we prove (a) by a nested induction. His outer induction is on the number of times the algorithm added transitions to the  $P$ -automaton. We instead prove the lemma by induction on the transitive reflexive closure of  $pre\_star\_rule$ . The inner induction is more challenging to formalize. Here, Schwoon considers a specific transition  $t$  which he defines as the  $i$ th transition added to  $P$ -automaton  $A$ . In the same context he considers a word  $w$  and two states,  $\text{Init } p$  and  $q$ , such that  $(\text{Init } p, w, q) \in \text{LTS.trans\_star } A'$ . He then defines  $j$  as the number of times  $t$  is used in  $(\text{Init } p, w, q) \in \text{LTS.trans\_star } A'$ . We may argue that this number is not well-defined, because there can be several paths from  $\text{Init } p$  to  $q$  consuming  $w$ , and on these paths  $t$  may not occur the same number of times. It turns out we can choose among these paths completely freely—any one of them will work, and so we just choose one arbitrarily. Formalizing this required us to define a variant of  $trans\_star$  that keeps track of the intermediate states.

### B. Nondeterministic $post^*$ Saturation

We call states with no incoming or outgoing transitions isolated. The  $post^*$  saturation algorithm requires the addition

of new noninitial states that are isolated in the automaton on which the algorithm is run. Under certain conditions the algorithm adds transitions into and out of these. Each such new state corresponds to a control location and a label. We extend the datatype of states with a new constructor `Isolated` for these:

```
datatype ('ctr_loc, 'noninit, 'label) state =
  Init 'ctr_loc | Noninit 'noninit | Isolated 'ctr_loc 'label
```

Moreover, we define `isols = {q. ∃p. q = Isolated p}`.

Steps in the  $post^*$  saturation are formalized as follows:

**inductive** `post_star_rules` **where**

```
(p, γ) ↦ (p', pop) ⟶ (Init p', ε, q) ∉ A ⟶
(Init p, [γ], q) ∈ LTS_ε.trans_star_ε A ⟶
post_star_rules A (A ∪ {(Init p', ε, q)})
| (p, γ) ↦ (p', swap γ') ⟶ (Init p', Some γ', q) ∉ A ⟶
(Init p, [γ], q) ∈ LTS_ε.trans_star_ε A ⟶
post_star_rules A (A ∪ {(Init p', Some γ', q)})
| (p, γ) ↦ (p', push γ' γ'') ⟶
(Init p', Some γ', Isolated p' γ') ∉ A ⟶
(Init p, [γ], q) ∈ LTS_ε.trans_star_ε A ⟶
post_star_rules A (A ∪ {(Init p', Some γ', Isolated p' γ')})
| (p, γ) ↦ (p', push γ' γ'') ⟶
(Isolated p' γ', Some γ'', q) ∉ A ⟶
(Init p', Some γ', Isolated p' γ') ∈ A ⟶
(Init p, [γ], q) ∈ LTS_ε.trans_star_ε A ⟶
post_star_rules A (A ∪ {(Isolated p' γ', Some γ'', q)})
```

The relation has one rule for `pop`, one for `swap`, and two for `push`. It uses `LTS_ε.trans_star_ε`, which is similar to `LTS.trans_star` but allows  $\varepsilon$ -transitions that do not consume stack symbols. The transition  $(\text{Init } p', \varepsilon, q)$  is an  $\varepsilon$ -transition and  $(\text{Init } p', \text{Some } \gamma', q)$  is a  $\gamma'$ -labeled non- $\varepsilon$ -transition. The function `lang_ε` returns the language of a  $P$ -automaton with  $\varepsilon$ -transitions. We prove  $post^*$  saturation correct:

**theorem** `post_star_rules_correct`:

```
assumes saturation post_star_rules A A'
and inits ⊆ LTS.srcs A and isols ⊆ LTS.isolated A
shows lang_ε A' = post_star (lang_ε A)
```

Schwoon's definition of the  $post^*$  rule has only one rule for `push` (in contrast to our two rules). In his rule, Schwoon *first* adds a transition  $(\text{Init } p', \text{Some } \gamma', \text{Isolated } p' \gamma')$  and *then* adds a transition  $(\text{Isolated } p' \gamma', \text{Some } \gamma'', q)$ . Consider his rule here presented in his formulation but our notation:

```
If (p, γ) ↦ (p', push γ' γ'') and
(Init p, γ, q) ∈ LTS_ε.trans_star_ε A,
first add (Init p', Some γ', Isolated p' γ');
then add (Isolated p' γ', Some γ'', q).
```

We were at first surprised that he specified this *first/then* order, but his correctness proof actually relies on it. Specifically, the order is used in his proof of Lemma 3.4, which is the key to prove the  $\supseteq$  direction of `post_star_rules_correct`. We present Lemma 3.4 in Schwoon's formulation but our notation:

**Lemma 3.4** If saturation `post_star_rules A A'` and  $(\text{Init } p, w, q) \in \text{LTS}_\varepsilon.\text{trans\_star}_\varepsilon A'$  then:

- (a) if  $q \notin \text{isols}$ , then  $(p', w') \Rightarrow^* (p, w)$  for a configuration  $(p', w')$  such that  $(\text{Init } p', w', q) \in \text{LTS}_\varepsilon.\text{trans\_star}_\varepsilon A$ ;

- (b) if  $q = \text{Isolated } p' \gamma'$ , then  $(p', \gamma') \Rightarrow^* (p, w)$ .

Schwoon's proof is a nested induction. The outer induction is on the number of transitions  $post^*$  has added. The induction step proceeds by an inner induction on the number of times the most recently added transition  $t$  was used in  $(\text{Init } p, w, q') \in \text{LTS}_\varepsilon.\text{trans\_star}_\varepsilon A'$ . (We resolve the ambiguity of that number's meaning in a similar way as for  $pre^*$ .) The proof then proceeds by a case distinction on which of the  $post^*$  saturation rules added  $t$ . Consider the case where  $t$  was added by the "first" part of the rule for `push`. In this case,  $t$  has the form  $(\text{Init } p', \text{Some } \gamma', \text{Isolated } p' \gamma')$ . Schwoon states that "Then since `Isolated`  $p' \gamma'$  has no transitions leading into it initially, it cannot have played part in an application rule before this step, and  $t$  is the first transition leading to it. Also, there are no transitions leading away from  $t$  so far." Had Schwoon not forced the algorithm to *first* add the transition into `Isolated`  $p' \gamma'$  and *then* add the one out of it, then he could not have claimed that there are no transition leading away from  $t$ . We capture this idea in the following two lemmas, stating that if  $t$  is not present, then `Isolated`  $p' \gamma'$  must be a source and a sink:

**lemma** `post_star_rules_Isolated_source_invariant`:

```
assumes post_star_rules** A A'
and isols ⊆ LTS.isolated A
and (Init p', Some γ', Isolated p' γ') ∉ A'
shows Isolated p' γ' ∈ LTS.srcs A'
```

**lemma** `post_star_rules_Isolated_sink_invariant`:

```
assumes post_star_rules** A A'
and isols ⊆ LTS.isolated A
and (Init p', Some γ', Isolated p' γ') ∉ A'
shows Isolated p' γ' ∈ LTS.sinks A'
```

Formalizing Schwoon's `push` rule as a single rule in `post_star_rules` does not capture the order in which the two transition are added to the set. This is why we split the rule in two—one adding the transition into the new noninitial state and another adding the transition out of the new noninitial state. This does not yet impose the needed *first/then* order. However, we can impose the order by letting the latter rule be only applicable if the transition added by the former is indeed already in the automaton. This is possible because the transition added into state `Isolated`  $p' \gamma'$  is  $(\text{Init } p', \text{Some } \gamma', \text{Isolated } p' \gamma')$ , and thus we can refer to the states comprising this transition in any context where `Isolated`  $p' \gamma'$  is available, in particular, the second `push` rule. Note that our  $post^*$  saturation algorithm is slightly more general than Schwoon's as we do not require the transition out of the new noninitial state to be added immediately after the transition into it, rather we allow this to happen at any time after.

### C. Combined $dual^*$ Saturation

We now consider the recent bi-directional search approach, called  $dual^*$  [23]. With  $dual^*$  we can check if the configurations of one  $P$ -automaton  $A_2$  are reachable from another  $P$ -automaton  $A_1$  by alternating between saturating  $A_2$  towards its  $pre^*$  closure and  $A_1$  towards its  $post^*$  closure, while simultaneously (on-the-fly) keeping track of their intersection

**fun** (in LTS) reach where  
 reach  $p \ [] = \{p\}$   
 | reach  $p (\gamma \# w) = (\bigcup q' \in (\bigcup (p', \gamma', q') \in \text{step}.$   
   **if**  $p' = p \wedge \gamma' = \gamma$  **then**  $\{q'\}$  **else**  $\{\}$ ). reach  $q' w$ )

**definition** (in PDS) pre\_star1  $A = (\bigcup ((p, \gamma), (p', w)) \in \Delta.$   
 $\bigcup q \in \text{LTS.reach } A (\text{Init } p') (\text{lbl } w). \{\text{Init } p, \gamma, q\}\}$ )

**definition** (in PDS) pre\_star\_exec = the  $\circ$  while\_option  
 $(\lambda s. s \cup \text{pre\_star1 } s \neq s) (\lambda s. s \cup \text{pre\_star1 } s)$

Fig. 4: Executable  $pre^*$

automaton. As soon as the intersection automaton becomes nonempty, we know that there is a state in  $A_2$  that is reachable from  $A_1$ . This is the case even if the  $pre^*$  and  $post^*$  automata are not saturated. Our correctness theorem is formalized here:

**theorem** dual\_star\_correct\_early\_termination:

**assumes**  $\text{inits} \subseteq \text{LTS.srcs } A_1$  **and**  $\text{inits} \subseteq \text{LTS.srcs } A_2$   
**and**  $\text{isols} \subseteq \text{LTS.isolated } A_1 \cap \text{LTS.isolated } A_2$   
**and**  $\text{post\_star\_rules}^{**} A_1 A_1'$  **and**  $\text{pre\_star\_rule}^{**} A_2 A_2'$   
**and**  $\text{lang\_}\varepsilon\text{-inters} (\text{inters\_}\varepsilon A_1' (\text{LTS\_}\varepsilon\text{-of } A_2')) \neq \{\}$   
**shows**  $\exists c_1 \in \text{lang\_}\varepsilon A_1. \exists c_2 \in \text{lang } A_2. c_1 \Rightarrow^* c_2$

The function  $\text{LTS\_}\varepsilon\text{-of}$  trivially converts a  $P$ -automaton to a  $P$ -automaton with  $\varepsilon$ -transitions. The function  $\text{inters\_}\varepsilon$  calculates the intersection  $P$ -automaton with  $\varepsilon$ -transitions of two  $P$ -automata with  $\varepsilon$ -transitions using a product construction. The function  $\text{lang\_}\varepsilon\text{-inters}$  gives the language of an intersection automaton. Since the  $\subseteq$  directions of  $\text{pre\_star\_rule\_correct}$  and  $\text{post\_star\_rules\_correct}$  do not rely on  $A'$  being saturated we prove them assuming only respectively  $\text{pre\_star\_rule}^{**} A_2 A_2'$  and  $\text{post\_star\_rules}^{**} A_1 A_1'$  instead of saturation  $\text{pre\_star\_rule } A_2 A_2'$  and saturation  $\text{post\_star\_rules } A_1 A_1'$ . We use these more general lemmas to prove *dual\_star\_correct\_early\_termination*.

#### IV. EXECUTABLE PUSHDOWN REACHABILITY

To get an executable algorithm for  $pre^*$ , we resolve the non-determinism by defining a functional program  $\text{pre\_star\_exec}$ , presented in Fig. 4 (where we indicate the corresponding locale for each definition), with this characteristic property:

**theorem** pre\_star\_exec\_language\_correct:

**assumes**  $\text{inits} \subseteq \text{LTS.srcs } A$   
**shows**  $\text{lang } (\text{pre\_star\_exec } A) = \text{pre\_star } (\text{lang } A)$

The function  $\text{reach}$  is  $\text{trans\_star}$ 's executable counterpart: for a state  $p$  and a word  $w$ ,  $\text{reach } p w$  computes the set of states reachable from  $p$  via  $w$  using  $\text{step}$  (fixed in the LTS locale). In other words, we have  $q \in \text{reach } p w$  iff  $(p, w, q) \in \text{trans\_star}$ .

The definition of  $\text{pre\_star\_exec}$  uses  $\text{while\_option}$ , the functional while loop counterpart. Given a test predicate  $b$ , a loop body  $c$  and a loop state  $s$ , the expression  $\text{while\_option } b c s$  computes the optional state  $\text{Some } (c (\dots (c (c s))))$  not satisfying  $b$  with the minimal number of applications of  $c$ , or  $\text{None}$  if no such state exists. Our specific loop keeps adding the results of a single step  $\text{pre\_star1}$  to the  $P$ -automaton comprising the loop state. We prove that our loop never returns  $\text{None}$ ,

**definition** nonempty  $A P Q =$   
 $(\exists p \in P. \exists q \in Q. \exists w. (p, w, q) \in \text{trans\_star } A)$

**definition** inters  $A B =$   
 $\{((p_1, p_2), w, (q_1, q_2)). (p_1, w, q_1) \in A \wedge (p_2, w, q_2) \in B\}$

**definition** nonempty\_inter  $\Delta A_1 F_1 F_1^{ni} A_2 F_2 F_2^{ni} =$   
 $\text{nonempty } (\text{inters } A_1 (\text{pre\_star\_exec } \Delta A_2))$   
 $((\lambda x. (x, x)) \text{ `inits } (\text{finals } F_1 F_1^{ni} \times \text{finals } F_2 F_2^{ni}))$

**definition** check  $\Delta A_1 F_1 F_1^{ni} A_2 F_2 F_2^{ni} =$   
 $(\text{if } \neg \text{inits} \subseteq \text{LTS.srcs } A_2 \text{ then } \text{None}$   
 $\text{else } \text{Some } (\text{nonempty\_inter } \Delta A_1 F_1 F_1^{ni} A_2 F_2 F_2^{ni}))$

Fig. 5: Reachability check for  $P$ -automata

i.e., it always terminates. We thus use the, defined partially as the  $(\text{Some } x) = x$ , in  $\text{pre\_star\_exec}$  to extract the resulting  $P$ -automaton. The step  $\text{pre\_star1}$  computes the set of all transitions that can be added by a single application of  $\text{pre\_star\_rule}$ .

Fig. 4's definitions are executable: Isabelle can interpret them as functional programs and extract Standard ML, Haskell, OCaml, or Scala code [19], but it is usually not possible to extract code for inductive predicates (such as  $\text{trans\_star}$  or the transitive closure in saturation) or definitions involving quantifiers ranging over an infinite domain (as in saturated). The definition of  $\text{pre\_star\_exec}$  has an obvious inefficiency. In every iteration,  $\text{pre\_star1}$  is evaluated twice: once as a part of the loop body and once as a part of the test. Instead we use the following improved equation, which replaces  $\text{while\_option}$  with explicit recursion, for code extraction.

**lemma** pre\_star\_exec\_code[code]:

$\text{pre\_star\_exec } s = (\text{let } s' = \text{pre\_star1 } s \text{ in}$   
 $\text{if } s' \subseteq s \text{ then } s \text{ else } \text{pre\_star\_exec } (s \cup s'))$

With the executable algorithm for  $pre^*$ , we decide the reachability problem for  $P$ -automata using the check function shown in Fig. 5. It inputs a PDS  $\Delta$  along with two  $P$ -automata represented by their transition relations ( $A_1$  and  $A_2$ ), their final initial states ( $F_1$  and  $F_2$ ) and their final noninitial states ( $F_1^{ni}$  and  $F_2^{ni}$ ). The computation proceeds by intersecting (inters) the initial  $P$ -automaton with the  $pre^*$  saturation of the final  $P$ -automaton and checking the result's nonemptiness (nonempty). Fig. 5 refers to functions  $\text{pre\_star\_exec}$ ,  $\text{inits}$ ,  $\text{finals}$ , and  $\text{trans\_star}$  which we introduced earlier in the context of different locales, outside of the respective locale. Therefore, these functions take additional parameters that correspond to the fixed parameters of the respective locale if they are used by the function (e.g., we write  $\text{pre\_star\_exec } \Delta$  instead of  $\text{pre\_star\_exec}$  for an implicitly fixed  $\Delta$ ).

The definition of  $\text{nonempty}$  is not executable because of the quantification over words  $w$ . We implement, but omit here, the straightforward executable algorithm that starts with the set of initial states  $P$  and iteratively adds transitions from  $A$  until it reaches  $Q$  or saturates without reaching  $Q$ , in which case the language is empty since no state in  $Q$  is reachable from  $P$ .

Overall,  $\text{check}$  returns an optional Boolean value, where  $\text{None}$  signifies a well-formedness violation on the final

$P$ -automaton: a non-source initial state in  $A_2$ . If check returns Some  $b$ , then  $b$  is the answer to the reachability problem for  $P$ -automata. We formalize this characterization of check by the following two theorems (phrased outside of locales).

**theorem** *check\_None*:

$$\text{check } \Delta \ A_1 \ F_1 \ F_1^{\text{ni}} \ A_2 \ F_2 \ F_2^{\text{ni}} = \text{None} \longleftrightarrow \\ \neg \text{inits} \subseteq \text{LTS.srscs } A_2$$

**theorem** *check\_Some*:

$$\text{check } \Delta \ A_1 \ F_1 \ F_1^{\text{ni}} \ A_2 \ F_2 \ F_2^{\text{ni}} = \text{Some } b \longleftrightarrow \\ (\text{inits} \subseteq \text{LTS.srscs } A_2 \wedge (b \longleftrightarrow \\ (\exists p \ w \ p' \ w'. \text{step\_starp } \Delta \ (p, w) \ (p', w') \wedge \\ (p, w) \in \text{lang } A_1 \ F_1 \ F_1^{\text{ni}} \wedge (p', w') \in \text{lang } A_2 \ F_2 \ F_2^{\text{ni}})))$$

## V. DIFFERENTIAL TESTING

Differential testing [14], [18], [34] is a technique for finding implementation errors by executing different algorithms solving the same problem on a set of test cases and comparing the outputs. Differential testing has been effective for finding errors in a wide range of domains, from network certificate validation [47] to JVM implementations [8]. Yet, even different algorithms do not necessarily fail independently, e.g., when built from the same specification [27] or when sharing potentially faulty components, e.g., input parsers or preprocessing. To reduce the danger of missing such errors, we suggest to incorporate a formally verified implementation in differential testing. Moreover, in case of a discrepancy the verified oracle reliably tells us which of the unverified implementations is wrong.

### A. Differential Testing of Pushdown Reachability

Our executable formalization of pushdown reachability allows us to perform differential testing on unverified tools for the same problem. A test case for pushdown reachability consists of a PDS with rules  $\Delta$  and two  $P$ -automata  $\mathcal{A}_1$  and  $\mathcal{A}_2$  representing the initial and final configurations of interest. The answer to the test case is whether there exist  $c \in L(\mathcal{A}_1)$  and  $c' \in L(\mathcal{A}_2)$  such that  $c \Rightarrow^* c'$  using the rules  $\Delta$ .

To execute the formalization on a given test case, we generate an Isabelle theory file, which first defines the control locations, labels, and automata states as finite subsets of the natural numbers (their sizes depending on the specific test case), and then includes for the pushdown rules  $\Delta$  and the two  $P$ -automata, each represented by its transitions  $A_i$  along with the accepting (initial and noninitial) states  $F_i$  and  $F_i^{\text{ni}}$  for  $i \in \{1, 2\}$ . Fig. 3 shows a specific example of  $\Delta$  and  $A$  definitions.

We generate a lemma that uses our check function, where the expected result Some True or Some False is inserted depending on the answer produced by an unverified tool under test (invoked before generating the theory on the same inputs):

**lemma**  $\text{check } \Delta \ A_1 \ F_1 \ F_1^{\text{ni}} \ A_2 \ F_2 \ F_2^{\text{ni}} = \text{Some True}$  **by** eval

The eval proof method extracts Standard ML code for check and other constants in the lemma and executes the lemma statement as an expression. It succeeds iff the lemma evaluates to True. We call a test case a counter-example, if the proof method fails. One could also run the extracted code outside

**Input:** Reachability tools *tool* and *oracle*, PDS  $(P, \Gamma, \Delta)$ ,  $P$ -automata  $\mathcal{A}_i = (P \cup N_i, \rightarrow_i, P, F_i)$  for  $i \in \{1, 2\}$ .

**Output:** Minimal counter-example (failing testcase)

- 1:  $c \leftarrow \Delta \cup (\{1\} \times (\rightarrow_1 \cup F_1)) \cup (\{2\} \times (\rightarrow_2 \cup F_2))$   
▷ Convert to a set of features
- 2: **return** DD( $c$ , 2) ▷ returned set of features can be converted to PDS and  $P$ -automata as on lines 10-11
- 3: **function** DD( $c$ ,  $n$ ) ▷  $c$  is a test case,  $n$  is granularity
- 4: let  $c_1 \uplus \dots \uplus c_n = c$ , all  $c_i$  as evenly sized as possible
- 5: **if**  $\exists i. \text{BAD}(c_i)$  **return** DD( $c_i$ , 2)
- 6: **else if**  $\exists i. \text{BAD}(c \setminus c_i)$  **return** DD( $c \setminus c_i$ ,  $\max(n-1, 2)$ )
- 7: **else if**  $n < |c|$  **return** DD( $c$ ,  $\min(2n, |c|)$ )
- 8: **else return**  $c$
- 9: **function** BAD( $c$ ) ▷  $c$  is a test case
- 10: let  $\Delta' = c \cap \Delta$  ▷ extract PDS rules and  $P$ -automata
- 11: for  $i \in \{1, 2\}$  let  $\mathcal{A}'_i = (P \cup N_i, \rightarrow'_i, P, F'_i)$  where  
 $\rightarrow'_i = \{t \in \rightarrow_i \mid (i, t) \in c\}$  and  $F'_i = \{q \in F_i \mid (i, q) \in c\}$
- 12: with both tools check if  $\mathcal{A}'_1$  reaches  $\mathcal{A}'_2$  via  $(P, \Gamma, \Delta')$
- 13: **return false** if *tool* and *oracle* agree, else *true*

Algorithm 1: Specialization of delta-debugging [51] to PDS.

Isabelle, but our setup allows us to generate the inputs to check on the formalization level instead of that of the extracted code.

To efficiently check a large number of test cases, we batch multiple definitions and lemmas into one theory file, thus reducing the overhead of starting Isabelle. We run Isabelle from the command line and check the output log for any failing eval proofs, which correspond to failing test cases.

### B. Automatic Counter-Example Minimization

If differential testing finds a failing test case, we use delta-debugging [51] to automatically reduce it to a minimal failing test case to help the subsequent debugging process. We use the minimizing delta debugging algorithm [51] that sees a test case as a set of features, and works by systematically testing different subsets until a minimal failing test case is found.

We use delta debugging on any discovered counter-example and fix the set of features to contain: (i) each pushdown rule, (ii) each transition in either of the  $P$ -automata, and (iii) each final state in a  $P$ -automaton (as opposed to it not being final).

States and labels are identified by unique names, and the initial  $P$ -automata states are exactly the states mentioned in any pushdown rule in the feature set. We specialize the general delta debugging algorithm to pushdown systems as shown in Algorithm 1. The algorithm first creates the set of features and calls the recursive function DD with this set of features and the granularity 2. The function then splits the set of features into a number of equally sized subsets (according to the granularity) and checks if any of these subsets or their complements still fail. If yes, then the function tries to recursively reduce the set of features further, otherwise it will increase the granularity and try again. The function BAD converts the set

$\Delta = \{$	$(p_0, B) \leftrightarrow (p_2, \text{push } D B),$	$(p_1, B) \leftrightarrow (p_1, \text{swap } C),$	$(p_2, C) \leftrightarrow (p_0, \text{push } C C),$	$(p_2, B) \leftrightarrow (p_2, \text{swap } E),$	$(p_3, E) \leftrightarrow (p_2, \text{swap } C),$
$(p_0, D) \leftrightarrow (p_0, \text{swap } A),$	$(p_0, C) \leftrightarrow (p_2, \text{swap } D),$	$(p_1, E) \leftrightarrow (p_1, \text{swap } C),$	$(p_2, D) \leftrightarrow$	$(p_2, E) \leftrightarrow (p_3, \text{push } A E),$	$(p_3, B) \leftrightarrow (p_2, \text{push } D B),$
$(p_0, E) \leftrightarrow (p_0, \text{push } B E),$	$(p_0, E) \leftrightarrow (p_2, \text{swap } E),$	$(p_1, A) \leftrightarrow (p_2, \text{swap } A),$	$(p_0, \text{push } B D),$	$(p_2, B) \leftrightarrow (p_3, \text{push } C B),$	$(p_3, E) \leftrightarrow (p_3, \text{swap } A),$
$(p_0, D) \leftrightarrow$	$(p_0, E) \leftrightarrow (p_3, \text{push } B E),$	$(p_1, D) \leftrightarrow (p_2, \text{swap } D),$	$(p_2, C) \leftrightarrow (p_1, \text{push } C C),$	$(p_3, D) \leftrightarrow$	$(p_3, A) \leftrightarrow (p_3, \text{push } C A),$
$(p_0, \text{push } D D),$	$(p_0, C) \leftrightarrow (p_3, \text{swap } E),$	$(p_1, C) \leftrightarrow (p_2, \text{swap } E),$	$(p_2, A) \leftrightarrow (p_1, \text{push } B A),$	$(p_0, \text{push } B D),$	$(p_3, E) \leftrightarrow (p_3, \text{swap } D),$
$(p_0, D) \leftrightarrow (p_0, \text{pop}),$	$(p_1, B) \leftrightarrow (p_0, \text{swap } C),$	$(p_1, C) \leftrightarrow (p_3, \text{swap } D),$	$(p_2, A) \leftrightarrow (p_2, \text{push } A A),$	$(p_3, C) \leftrightarrow (p_0, \text{push } E C),$	$(p_3, C) \leftrightarrow (p_3, \text{pop})\}$
$(p_0, D) \leftrightarrow (p_1, \text{swap } A),$	$(p_1, D) \leftrightarrow (p_0, \text{swap } C),$	$(p_1, D) \leftrightarrow (p_3, \text{pop}),$	$(p_2, C) \leftrightarrow (p_2, \text{swap } A),$	$(p_3, C) \leftrightarrow (p_0, \text{swap } E),$	
$(p_0, A) \leftrightarrow (p_1, \text{push } C A),$	$(p_1, C) \leftrightarrow (p_0, \text{swap } B),$	$(p_2, B) \leftrightarrow (p_0, \text{push } A B),$	$(p_2, E) \leftrightarrow (p_2, \text{swap } A),$	$(p_3, C) \leftrightarrow (p_1, \text{push } A C),$	
$(p_0, E) \leftrightarrow (p_2, \text{push } A E),$	$(p_1, C) \leftrightarrow (p_0, \text{swap } E),$	$(p_2, A) \leftrightarrow (p_0, \text{push } C A),$	$(p_2, A) \leftrightarrow (p_2, \text{push } B A),$	$(p_3, B) \leftrightarrow (p_1, \text{pop}),$	
$A_1 =$	$\Delta = \{(p_0, D) \leftrightarrow (p_0, \text{pop})\}$				
$\{(Init\ p_0, B, Noninit\ q_1), (Init\ p_0, D, Noninit\ q_0),$	$A_1 = \{(Init\ p_0, D, Noninit\ q_0), (Noninit\ q_0, D, Noninit\ q_1)\}$				
$(Init\ p_2, B, Noninit\ q_0), (Init\ p_3, A, Noninit\ q_2),$	$F_1 = \{ \} \quad F_1^{ni} = \{q_1\}$				
$(Noninit\ q_0, D, Noninit\ q_1), (Noninit\ q_2, C, Noninit\ q_0)\}$	$A_2 = \{ \}$				
$F_1 = \{ \} \quad F_1^{ni} = \{q_1\}$	$F_2 = \{p_0\} \quad F_2^{ni} = \{ \}$				
$A_2 = \{(Init\ p_2, A, Noninit\ q_0), (Init\ p_2, B, Noninit\ q_0)\}$					
$F_2 = \{p_0, p_2\} \quad F_2^{ni} = \{ \}$					

Fig. 6: Original and minimized (bottom right) counter-example

of features into a reduced pushdown system and two reduced  $P$ -automata and checks if the given tool implementation is still inconsistent with the oracle. We note that minimal failing counter-examples are only locally minimal and not necessarily unique. Yet, minimization is effective and necessary. Fig. 6 shows a real bug example we discovered by random differential testing in the PDAAAL library for pushdown reachability [23] and its minimization by Algorithm 1.

## VI. CASE STUDY: ANALYSIS OF PDAAAL

We apply differential testing with automatic counter-example minimization to PDAAAL [42], a recent C++ implementation of pushdown reachability checking, which appears to be the currently most efficient library for pushdown reachability [23]. PDAAAL implements  $post^*$ ,  $pre^*$  and  $dual^*$  [23].

These three different algorithms can be used in classical differential testing without a verified oracle, but given the large amount of shared code this is bound to miss some errors. And without a verified oracle, manual effort is needed to determine which implementation is faulty in case of discrepancies. This motivates using our verified reachability check via  $pre^*$ , and we compare the output of each unverified algorithm to the output of our trustworthy oracle on a large number of test cases.

### A. Methodology of Test Case Generation

We structure our test case generation in three phases.

In phase one, we use real-world tests generated from the domain of network verification, which PDAAAL was originally built for as a backend [22]. We generate pushdown reachability problems from realistic network verification use-cases on (up to) 100 random reachability queries on each of the 260 different networks derived from the Internet Topology Zoo [28] giving a total of 25 512 test cases.

In phase two, we randomly generate valid pushdown systems and  $P$ -automata. We generate 15 000 cases of varying sizes with 4 control locations, 5 labels, up to 200 pushdown rules, and up to 13 automata transitions. Our generator writes all ingredients (pushdown system and  $P$ -automata) to a JSON

file, which is then translated to the Isabelle definitions and correctness lemmas that incorporate the unverified answers.

Finally, in phase three, we exhaustively enumerate the set of all test cases up to a certain (small) size. For the pushdown systems  $|P| = |\Gamma| = 2$  and  $|\Delta| \leq 2$ , and for  $P$ -automata  $|N_1| = 2$ ,  $|N_2| = 1$  and  $|\rightarrow| \leq 2$ . We remove symmetric cases, where swapping state names or labels gives an identical case. In total, this yields close to 27 million combinations of pushdown systems and  $P$ -automata. For the exhaustive tests, we output both JSON files and Isabelle definitions directly from the test case generator. A bash script stitches together the Isabelle definitions into a single theory file with a batch of test cases to benefit from Isabelle’s parallel processing of proofs.

### B. Results

The real-world test cases showed no discrepancies between the verified oracle and PDAAAL. This indicates that PDAAAL has already been thoroughly tested on this type of problem instances. Isabelle ran out of memory in 30 of the 25 512 test cases. The average CPU time (on AMD EPYC 7642 processors at 1.5 GHz) per test case was 35 seconds for Isabelle, while PDAAAL used less than 0.02 seconds on most cases.

Phase two, however, resulted in 1 334 discrepancies. By applying our counter-example minimization, we noted that all these cases had a common trait: the  $P$ -automaton  $\mathcal{A}_2$  accepted the empty word. This helped us find the first implementation error in the implementation of the on-the-fly automata intersection when using  $post^*$ . The  $post^*$  algorithm can introduce  $\varepsilon$ -transitions, which were not handled correctly by the intersection implementation. In most cases, this does not matter, as for any  $\varepsilon$ -transition followed by a normal transition the  $post^*$  algorithm adds a direct transition at some later point. However, in the case of an empty stack being accepted by  $\mathcal{A}_2$ , this does not happen, which causes the unverified algorithm to return the wrong answer False. We resolved the error and re-ran the generated tests. After that only one discrepancy remained.

This second error was found in the implementation of  $pre^*$ . The minimized counter-example helped us find the source



10: **function** ADDTRANSITION( $q_i \xrightarrow{\gamma} q'_i$ ) ▷ with  $i \in \{1, 2\}$   
11:     **add**  $q_i \xrightarrow{\gamma} q'_i$  to  $\mathcal{A}_i$   
12:     **for all**  $q_{3-i}, q'_{3-i} \in Q_{3-i}$  s.t.  $(q_1, q_2) \in R$  and  $q_{3-i} \xrightarrow{\gamma} q'_{3-i}$  **do**  
13:         **add**  $(q_1, q_2) \xrightarrow{\gamma} (q'_1, q'_2)$  to  $\mathcal{A}_\cap$   
14:         ADDSTATE( $q'_1, q'_2$ )

(a) Snippet of (correct) intersection pseudocode by Jensen et al. [23]

```

@@ -119,10 +119,10 @@ namespace pdaaal {
119 119         if (res.second) { // New edge is not already in edges (rel U workset).
120 120             _workset.emplace(from, label, to);
121 121             if (trace != nullptr) { // Don't add existing edges
122 122 +             _automaton.add_edge(from, to, label, trace_ptr_from<W>(trace));
123 123                 if constexpr (ET) {
124 124                     _found = _found || _early_termination(from, label, to, trace_ptr_from<W>(trace));
125 125 -             _automaton.add_edge(from, to, label, trace_ptr_from<W>(trace));
126 126         }
127 127     }
128 128 };

```

(b) PDAAAL's C++ code showing the resolution of the second error

Fig. 7: Discovered second implementation error and its correct pseudocode

of the implementation error: the set of automata transitions was updated only after calling the function that performs the nonemptiness check of the intersection automaton, but it should have been updated before that call. We argue that this error is subtle, as it only causes a single failure out of 15 000 randomly generated test cases. Fig. 7a shows the correct pseudocode by Jensen et al. [23]. Fig. 7b shows PDAAAL's corresponding C++ code and the change resolving the error, where the line that needed to be moved corresponds to the pseudocode's Line 11.

For both errors, the affected test cases resulted in a correct answer for at least one of the other search strategies in PDAAAL. This is not the case for the last error, which is found in code shared by all three methods, and where PDAAAL's algorithms disagree only with Isabelle. This error is caused by a mismatch between the assumptions of the parser that builds the pushdown system and the data structure that stores the pushdown rules. The parser assumes that it can incrementally add rules to the data structure without knowing all labels in advance, but the data structure assumes to know all labels from the start to implement a memory optimization that replaces a rule that applies to all labels by a wildcard.

For the first two test phases, the program that generated Isabelle definitions also depended on this parser, so the bug was not discovered until the third phase, which has a different setup. After the three bugs were fixed, all test cases pass.

## VII. CONCLUSION

We presented a methodology that increases the reliability of tools and libraries for pushdown reachability analysis. To this

end, we formalized and proved in Isabelle/HOL the correctness of the essential saturation algorithms used in such tools. We extracted an executable program from our formalization and used it as a trustworthy oracle for differential testing. Putting the modern pushdown analysis library PDAAAL on the testbed, we discovered a number of implementation errors in its code, even though the library performed flawlessly in its application domain. Using our automatic counter-example minimization based on delta-debugging, we were able to identify the sources of these errors and suggested fixes to PDAAAL's implementation that now passes all the differential tests.

This process significantly increased PDAAAL's reliability and shows that with a moderate effort, the combination of proof assistants with code generation, differential testing, and delta-debugging is highly fruitful. The execution of all tests in the three phases took 303 CPU days. We executed the tests on a compute cluster with 1536 CPU cores. The formalization work took about two person-months for experienced formalizers, creating about 4 400 nonempty lines of Isabelle definition and proofs. An additional half person-month of work was needed to implement the differential testing and counter-example minimization, set up the tests, and localize and resolve the discovered errors. This one-time effort will also benefit the future development of PDAAAL.

Too often, the race for better performance can lead to subtle implementation errors. Our methodology shows how formally verified algorithms that were not tuned for performance can be used to improve the quality of tuned but unverified algorithms.

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